

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18 Stylesheet Version v18.0

> Title of Invention

High-Voltage Transistor With A Multi-Layer Conduction

Region

Application Number:

09/961229

≀E∯SIOLOGY CEITER 2800

Confirmation Number:

4375

First Named Applicant: Vladimir Rumennik Attorney Docket Number: 03692.P007XD4

Art Unit:

2811

Examiner:

Shouxiang Hu

Search string:

(6633065 or 6639277).pn.

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

i	nit	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
E		1	6633065	2003-10-14	Rumennik et al.	B2		
#4	700	2	6639277	2003-10-28	Rumennik et al.	B2		

Remarks

Note: Remarks are not for responding to an office action.

This information disclosure statement is being submitted concurrent with the filing of a Request for Continued Examination in the above-referenced application.

Signature

Examiner Name	Date
Shouswayfle	6/28/04



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Search string:

(4614959 or 5294824).pn.

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init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		
	1	4614959	1986-09-01	Nakagawa					
Se	2	5294824	1994-03-01	Okada					

Signature

Examiner Name		Date	
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